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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/565,837	01/25/2006	Young-Goo Song	21ST01555 US	7654

90323 7590 02/23/2010  
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EXAMINER
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KIM, RICHARD H

ART UNIT	PAPER NUMBER
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2871

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PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/565,837	<b>Applicant(s)</b> SONG ET AL.	
	<b>Examiner</b> RICHARD H. KIM	<b>Art Unit</b> 2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 09 October 2009.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1,3,5-7,9,11-13,15,17,18,20,22,23 and 26-29 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,3,5-7,9,11-13,15,17,18,20,22,23 and 26-29 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>12/21/09</u> .  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1, 3, 7, 9, 13, 15, 18, 20 and 26-29 rejected under 35 U.S.C. 102(e) as being anticipated by Kim et al. (US 6,859,254 B2).

3. Re claims 1, 7, 13 and 18, Kim et al. discloses a device and method in Figures 1-3 comprising an array substrate including a transparent substrate (3) including a display region that displays an image, a peripheral region having a driving circuit (col. 1, line 65) for displaying an image through the display region, and a sealine region (22) that surrounds the display region to define the display region and the peripheral region; a first insulation layer (37) formed over the transparent substrate, the first insulation layer having an opening (62) window in the sealine region; a pixel electrode (38) formed on the first insulation layer of the display region; and a

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second insulation layer (32) disposed between the first insulation layer and the transparent substrate, wherein the opening window extends through the first insulation layer to the second insulation layer and comprises at least one intermediate portion of the first insulation layer positioned in between the interior edges of the opening window wherein at least one intermediate portion consists primarily of the first insulation layer. The first insulation layer (37) is thicker than insulation layer (39) and therefore the intermediate portion consists primarily of the first insulation layer. Kim et al. further discloses a color filter substrate (5) facing the array substrate; a liquid crystal layer (17) interposed between the array substrate and the color filter substrate; and a sealing member (22) formed at the opening window to bond the array substrate and the color filter substrate.

4. Re claims 3, 9, 15 and 20, Kim et al. discloses that the first insulation layer corresponds to an organic layer (col. 2, lines 24, 25).

5. Re claims 26-29, Kim et al. discloses that a height of at least one intermediate portion is substantially the same as a height of the interior edges of the window opening (Fig. 3B, ref. 62).

### ***Claim Rejections - 35 USC § 103***

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 6 and 12 rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al. (US 6,859,254 B2) in view of Kim et al. (US 2004/0036815 A1).

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8. Kim et al. (US 6,859,254 B2) discloses the device previously recited, and further discloses the device comprising a switching device having a gate electrode (31), a drain electrode (36) that is electrically connected to the pixel electrode (38) and a source electrode (36), a gate line (11) electrically connected to the gate electrode, and a data line (13) that is electrically connected to the source electrode. However, the reference does not disclose that a portion of the data line overlaps with the pixel electrode.

9. Kim et al. (US 2004/0036815 A1) discloses a device wherein a portion of the data line (115) overlaps with the pixel electrode (120).

10. It would have been obvious to one having ordinary skill in the art at the time the invention was made for a portion of the data line to overlap with the pixel electrode since one would be motivated to reduce image deterioration (paragraph 15).

11. Claims 5, 11, 17 and 22 rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al. in view of Seo et al. (US 6,822,717 B2).

12. Kim et al. discloses the device previously recited, but fails to disclose that the second insulation layer corresponds to a silicon nitride layer (SiN<sub>x</sub>).

13. Seo et al discloses that the gate insulation layer can be formed of an inorganic materials, such as SiN<sub>x</sub> (col. 4, lines 20-24).

14. It would have been obvious to one having ordinary skill in the art to employ SiN<sub>x</sub> as the first insulation layer since SiN<sub>x</sub> is well known in the art to be used as an effective insulating layer material to insulate the gate lines and electrodes.

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15. Claim 23 rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al. (US 6,859,254 B2).

16. Kim et al. discloses the device previously recited, but fails to disclose that the liquid crystal layer is injected between the array substrate and the color filter substrate by a vacuum injection method.

17. It would have been obvious to one having ordinary skill in the art at the time the invention was made for the liquid crystal layer to be injected between the array substrate and the color filter substrate by a vacuum injection method. Examiner take Official Notice that injecting the liquid crystal layer using a vacuum injection method is well known in the art as an efficient and reliable means to forming the liquid crystal layer.

### ***Response to Arguments***

18. Applicant's arguments with respect to the claims have been considered but are moot in view of the new ground(s) of rejection.

### ***Conclusion***

19. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to RICHARD H. KIM whose telephone number is (571)272-2294. The examiner can normally be reached on 9:00-6:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Richard H Kim/

Primary Examiner, Art Unit 2871